

REMARKS

Claims 1-3 were rejected under 35 U.S.C. 112, second paragraph, as being indefinite. Claim 1 has been amended to more clearly recite the various interconnections of the recited elements. The claim is supported, for example, by Figure 2A where the first transistor is Q1, the second transistor is Q2, the third transistor is Q3 and the pair of trimming transistors are, for example, Q20/21 or Q4/14. Withdrawal of the Section 112 rejection is requested.

In view of the foregoing, Applicant respectfully submits that pending claims 1-20 are in condition for favorable action and allowance.

Respectfully submitted,

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